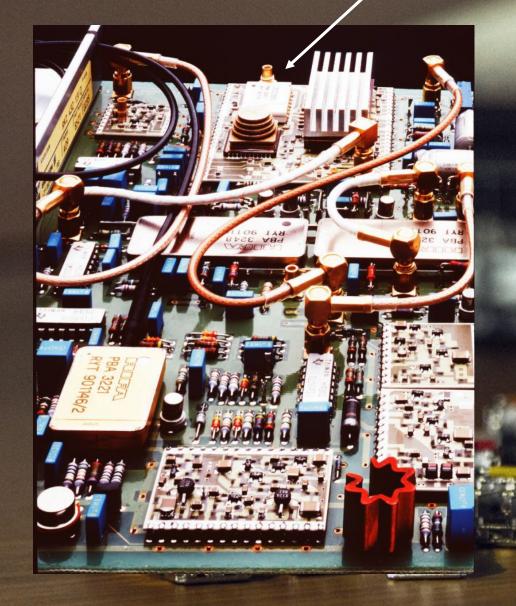
This is a 1987 state of the art 600Mbps "transceiver"



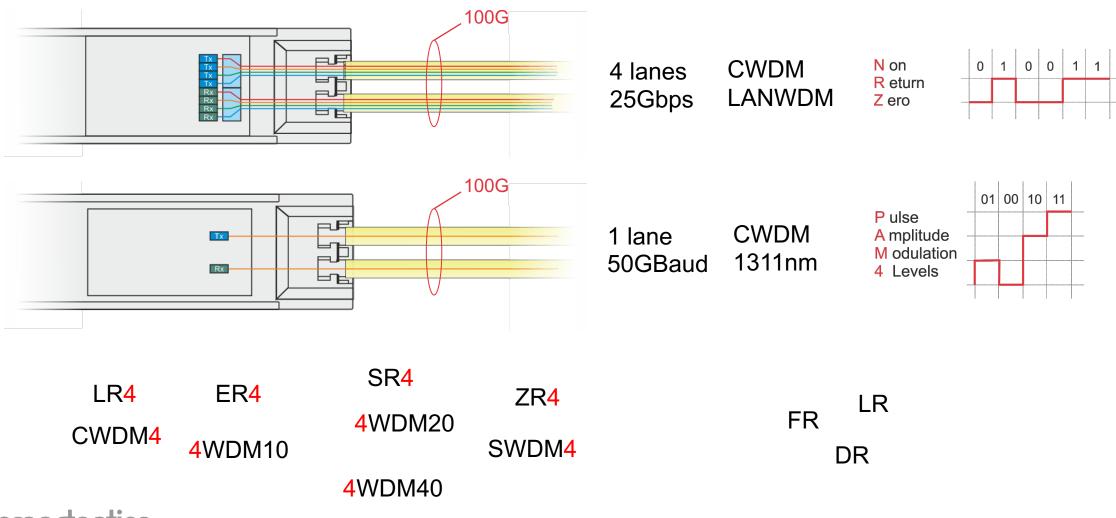
Patrik von Matern PLM Optical Devices Smartoptics

"Transceivers beyond 100G"

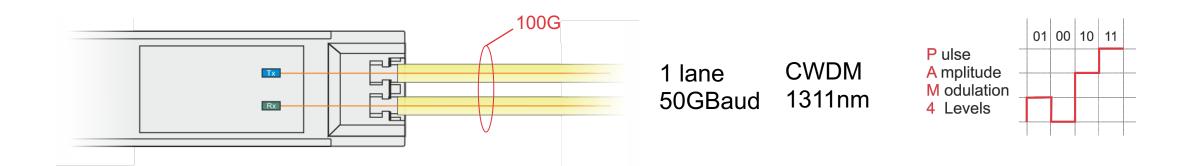
10 m



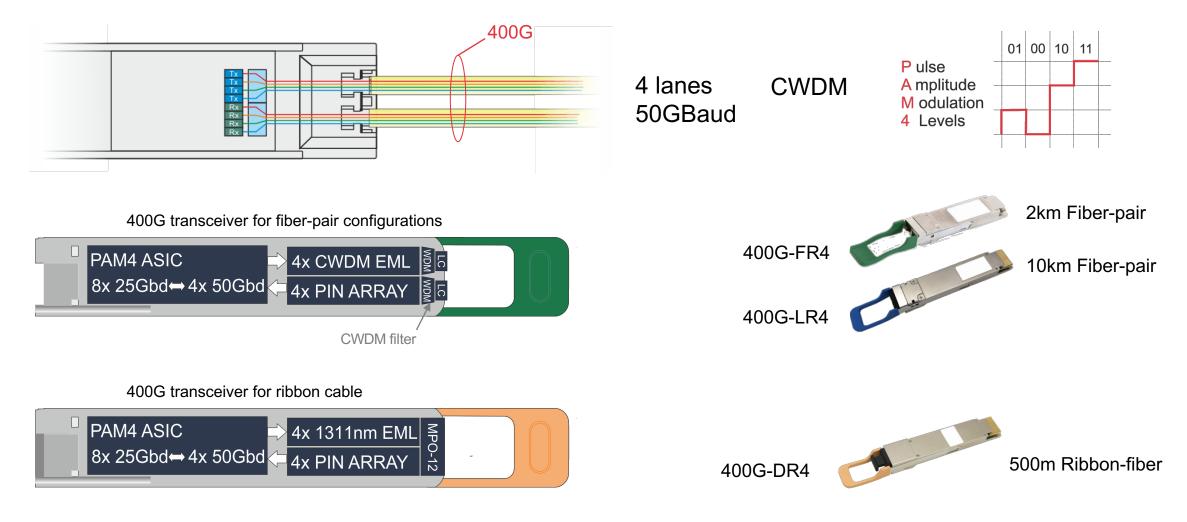
100G standards and MSA's for Grey optics



100G standards and MSA's for Grey optics



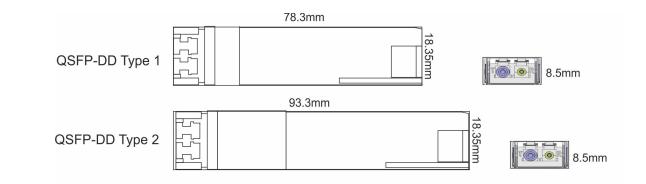
400G standards and MSA's for Grey optics



Step up in form-factor

Things that matter:

Size and Power!



QSFP-DD

- is longer than a QSFP28
- is backwards compatible with QSFP28
- has a higer power class capability than QSFP28

Power Class

1 2

3

4

5

6

7

8

QSFP-DD

Max 1.5W

3.5W

7.0W

8.0W

10.0W

12.0W

14.0W

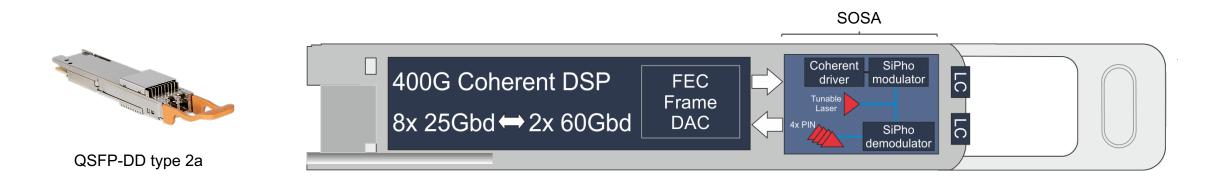
>14.0W

	QSFF	28
- F	Power Class	Max
	1	1.5W
	2	2.0W
P-DD	3	2.5W
	4	3.5W
	5	4.0W
	6	4.5W
	7	5.0W
	8	10W

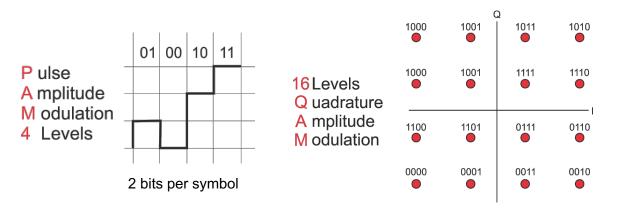
A typical 400G grey transceiver consumes ~ 10W

QSFP28 (Control of the second second

Coherent 400G



Electrical interface: 8 lanes PAM4 25Gbd / 50Gbps (400GAUI-8) for ZR and also 4x100GAUI-2 for ZR+. Internal conversion to 1 lane DP-16QAM, ~60Gbd / 400Gbps.



DP-16QAM (DP = Dual Polarization) 4 bits per symbol (x2)

smartoptics

AUI = Attachment Unit Interface 400GAUI-8 where 8 is number of lanes DSP: Digital Signaling Processor SOSA: Silicon Optical Sub-Assembly

400G Industry Standards

There are three principal initiatives to provide standardized 400G solutions.

		OpenZR+ MULTI-SOURCE AGREEMENT	Open ROADM
Target application	Edge DCI/Campus	Metro/Regional DCI	Metro/Regional DCI
Reach	80 – 120km / 11dB	>120km	>120km
Line capacity	400G	100G/200G/300G/400G	200G/300G/400G
Client formats	400GE	100GE/200GE/400GE	100GE-400GE + OTN
FEC	CFEC	oFEC	oFEC
Line modulation	16QAM	QPSK/8QAM/16QAM	QPSK/8QAM/16QAM

In short:	400G Ethernet	100/400G Ethernet	Ethernet + OTN
	Mid span meet	Performance	Performance
		Competition	ROADM network mgmt

Tx power challenges



QSFP-DD type 2a

400G ZR is challenging to mix with 10G to 100G channels due to low Tx power (-10dBm) and OSNR performance (26dB).

TECHNICAL DATA

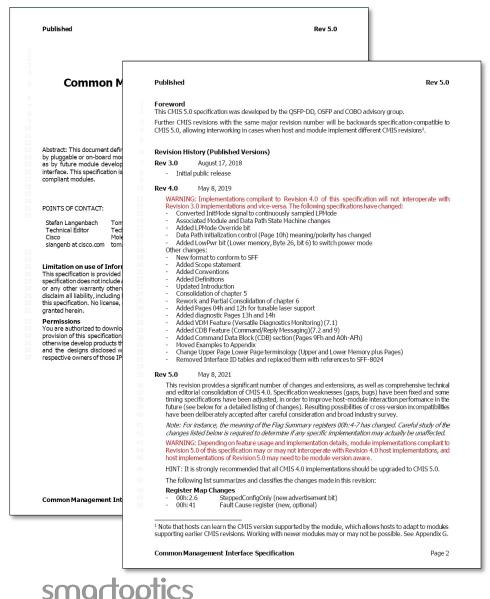
Parameter	Value			Parameter	Value		
Technology	DWDM QSFP-DD type 2			Transmitter data:			
Transmission media	SM (2x LC)		<	Output power	Min: -10.0dBm		
Typical reach	120km				Max: -6.0dBm		
Nominal wavelengths	192.00 - 196.00 THz			Transmit wavelengths	192.00 - 196.00 THz		
Interface standards	400GBASE-ZR				in 100 (75) GHz steps		
Protocol support	400GbE				(G.694.1)		
	4x 100GbE			Receiver data:			
Power consumption	< 17 W (Class 8)			Minimum input power	-20.0dBm 1)		
Operating temperature	0°C to +70°C				-12.0dBm ²⁾		
Storage temperature	-40°C to +85°C			Input sensitivity	-12.0dBm ³⁾		
1) Receiver sensitivity at unam	plified configurations			Overload (max power)	0 dBm ²⁾		
²⁾ Signal power of the channel at the OSNR performance value				OSNR tolerance	Max: 26dB/0.1nm 4)		
3) Input power needed to achie	ve post FEC BER			CD tolerance	Min: 2400ps/nm		
4) At CFEC threshold				Optical path OSNR penalty tolerance	Max: 0.5dB 5)		
⁵⁾ OSNR tolerance penalty over OSNR Tolerance due to reflections and dispersion				PMD tolerance	Min: 10 ps ⁸⁾		
6) Tolerance to PMD with <0.5	dB penalty to OSNR sensitivity.			DDM	Yes		
				MSA compliance	QSFP-DD MSA		
Safety/regulatory compliance:				-	CMIS4.0		
TUV/UL/FDA (contact Smartop			Men -				

The Smartoptics DCP-M Flexible Open Line Systems are designed to manage these signals.

smartoptics Line	921 922 Tx Rx Tx Rx	925 926 Tx Rx Tx Rx	929 930 Ta Ri Ta Ri	933 934 Tx Rx Tx Rx	937 938 Tx Rx Ta Ra	_	941 942 Tr Rr Tx Rr	945 926 Tx Rx Ts Rx	929 930 Tx Rx Ta Rx	953 954 Tx Rx Tx Rx	957 958 Te Re Tx Re	DCP-M40-C-ZR+	\Box
		0000	0000							0000			
						33							
Power Status	Rc Tx Rx Tx 923 924	Re Tx Rx Tx 927 928	8x Ta Ra Ta 931 932	Rc Tx Rx Tx 935 936	Re Tx Re Tx 939 940		8x Ta Ris Tx 943 944	947 948	Rx Tx Rx Tx 951 952	Rx Tx Rx Tx 955 956	Rx Ta Rx Tx 959 960		\square

RoHS compliance

400G and Common Management Interface Specification CMIS



Revision 3.0 was released in Aug 2018. A 3.0.1 update in Jan, 2019. Revision 4.0 was released in May 2019. Revision 5.0 was released in May 2021

Different system vendors have implemented different versions!

CMIS3.0 is not compatible with CMIS4.0 or CMIS5.0 and vice verse.

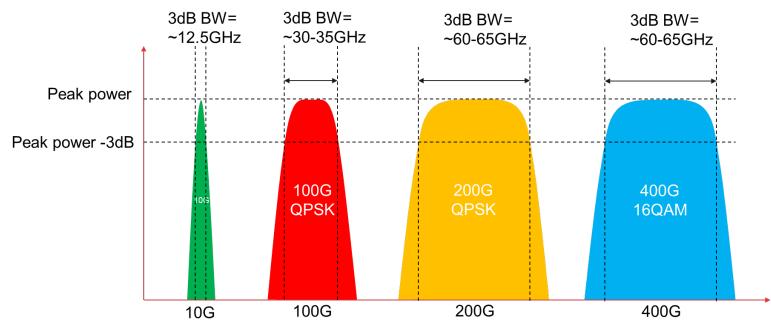
CMIS5.0 contains additions and bug fixes.

Imperative to check what version that the host equipment supports and select a transceiver having the corresponding CMIS version.

Used by both grey and coherent 400G transceivers!

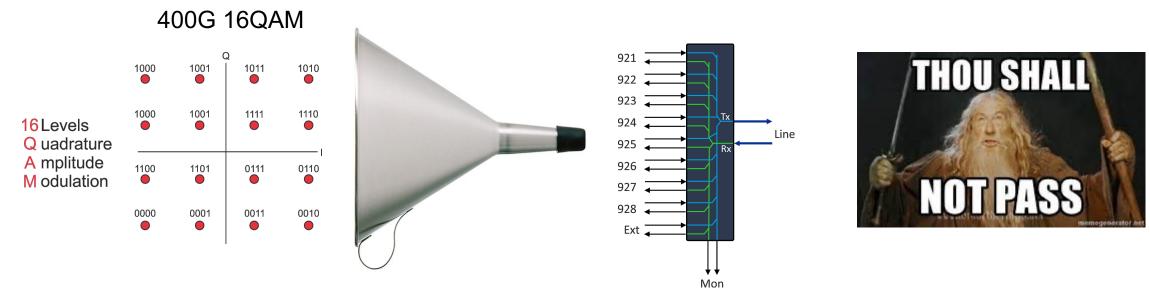
9

Coherent 400G capable filters

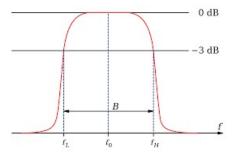


Signal spectrum for different rates and modulation formats.

400G Capable filters



400G 16QAM can be multiplexed @ 100GHz grid, but requires a wider channel passband than lower rates!



Must be > 75GHz

800G Technology

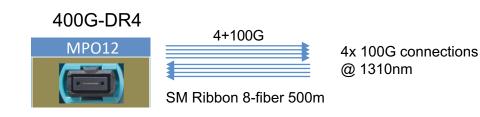


800G Grey optics

400G Transceivers

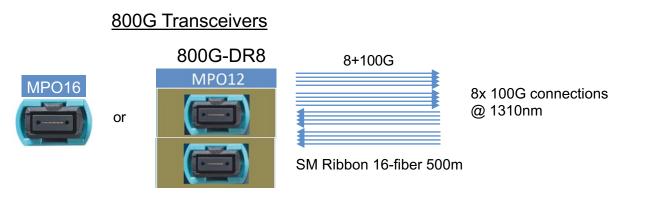


By doubling 400G technology!



Challenge!

Double number of optical connections! Size and power consumption of electronics and optics.

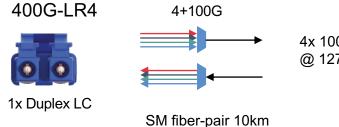


800G Grey optics

How is 800G achieved?

By doubling 400G technology!

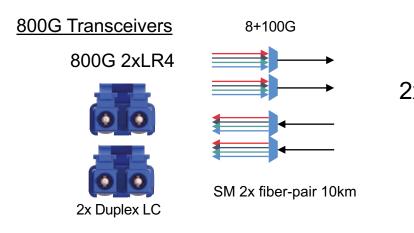
400G Transceivers



4x 100G connections @ 1271, 1291, 1311, 1331nm

Challenge!

Double number of optical connections! Size and power consumption of electronics and optics.



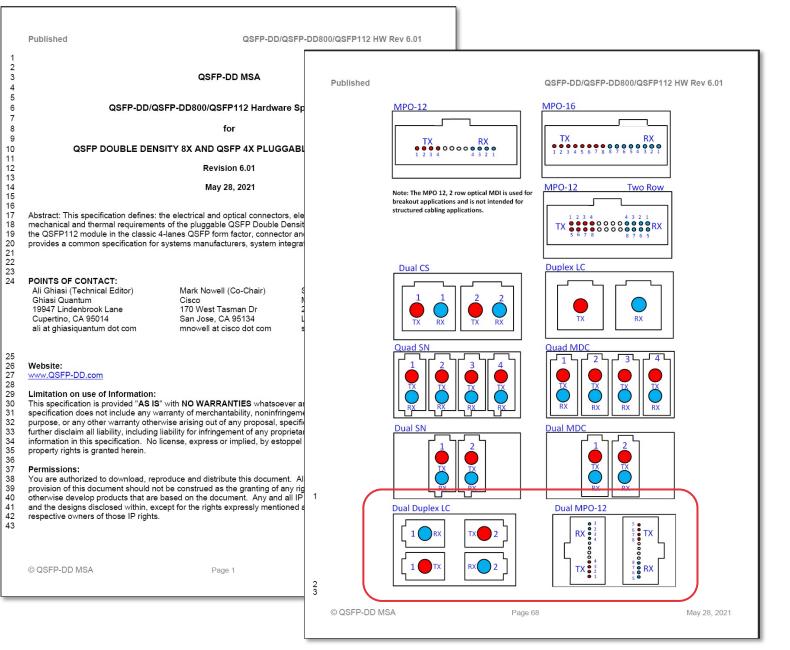
2x ^{4x 100G connections} @ 1271, 1291, 1311, 1331nm

Form-factors for 800G

Form-factor options:



New QSFP-DD MSA spec with multiple connector options!



Coherent 800G





Power consumption between 24-27W

"interoperable 800G coherent line specifications for campus and DCI applications".

Scope:

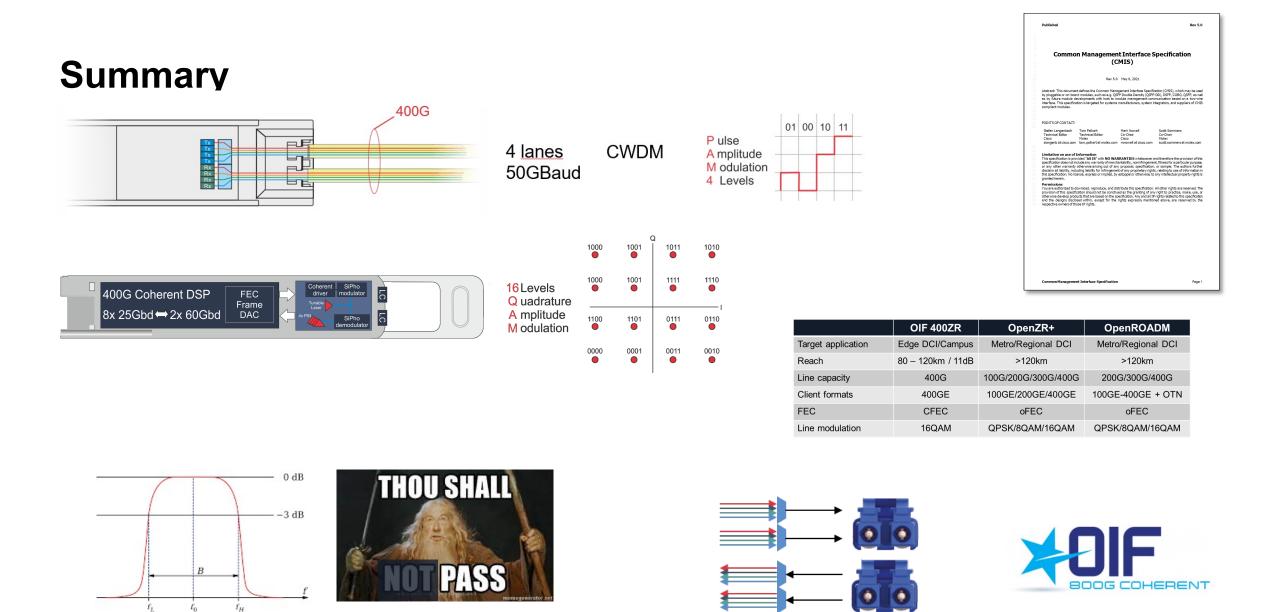
- Define single-lambda 800G coherent line interfaces for two applications:
 - Amplified, single span, DWDM links up to 80-120km
 - Unamplified, fixed wavelength links of 2-10km
- Support Ethernet client(s) (minimum 100GE) up to 800G aggregate bandwidth

First availability in second half of 2023.

	1000	1001 ●	ג 1011 •	1010
16 Levels Q uadrature	1000	1001	1111	1110
A mplitude M odulation	1100	1101	0111	0110
	0000	0001	0011	0010

Same modulation format as 400G, but with a higher baud rate.

Will require even wider channel filter passband than 400G.



smartoptics

Dual MPO-12

RX

TX

TX RX

Dual Duplex LC

1 **1** TX

rx 🔵 2

RX 2

